

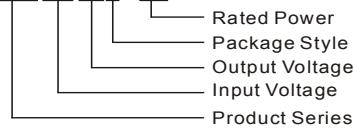


SWRA_S - 3WR2 & SWRB_S - 3WR2 Series

**3W, WIDE INPUT, ISOLATED & REGULATED
DUAL/SINGLE OUTPUT SIP PACKAGING, DC-DC
CONVERTER**

PART NUMBER SYSTEM

SWRB 12 05S- 3WR2



FEATURES

- Ultra-Miniature SIP Package
- 2:1 wide input voltage range
- Temperature range: -40°C ~ +85°C
- 1.5KVDC isolation
- Short Circuit Protection(automatic recovery)
- External On/Off control
- High Power Density
- Without overshoot when turning On/Off

APPLICATION

The SWRA_S-3WR2 & SWRB_S-3WR2 Series are specially designed for applications where a wide range input voltage power supplies are isolated from the input power supply in a distributed power supply system on a circuit board. For these DC-DC converters, You can reduce the design point of failure and save the development of micro power supply's manpower, material and time costs, also better ensure product quality stability, protect safety and reliability of the end of products.

These products apply to where:

- 1) Input voltage range ≤2:1;
- 2) 1.5KVDC input and output isolation;
- 3) Regulated and low ripple noise is required.
Such as: industrial control, tele-communications etc.

SELECTION GUIDE

Model Number	Input Voltage(VDC)		Output Voltage (VDC)	Output Current (mA)		@Max. Load	@No Load	Reflected Ripple Current (mA,Typ.)	Max. Capacitive Load(μF)	Efficiency (% , Typ.) @Max. Load
	Nominal (Range)	Max**		Max.	Min.					
SWRA0505S-3WR2	5 (4.5-9)	11	±5	±250	±13	715	40	30	1000	70
SWRA0512S-3WR2			±12	±104	±5	705			470	71
SWRA0515S-3WR2			±15	±83	±4	705			330	71
SWRB0505S-3WR2			5	500	25	725			2200	69
SWRB0512S-3WR2			12	208	11	715			680	70
SWRB0515S-3WR2			15	166	9	715			470	70
SWRA1205S-3WR2	12 (9-18)	20	±5	±300	±15	325	20	30	1000	77
SWRA1212S-3WR2			±12	±125	±6	317			470	79
SWRA1215S-3WR2			±15	±100	±5	313			330	80
SWRB1205S-3WR2			5	600	30	329			2200	76
SWRB1212S-3WR2			12	250	13	329			680	76
SWRB1215S-3WR2			15	200	10	329			470	76
SWRA2405S-3WR2	24 (18-36)	40	±5	±300	±15	163	7	110	1000	77
SWRA2412S-3WR2			±12	±125	±6	157			470	80
SWRA2415S-3WR2			±15	±100	±5	154			330	81
SWRB2405S-3WR2			5	600	30	160			2200	78
SWRB2412S-3WR2			12	250	13	154			680	81
SWRB2415S-3WR2			15	200	10	154			470	81
SWRB2424S-3WR2			24	125	6	152			330	82
SWRB4805S-3WR2	48 (36-75)	80	5	600	30	82	7	45	2200	76
SWRB4812S-3WR2			12	250	13	78			680	80
SWRB4815S-3WR2			15	200	10	76			470	82

Note:1. Designing.

2. **Input voltage can't exceed this value, or will cause the permanent damage.

INPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Input Surge Voltage (1sec. max.)	5V input	-0.7	--	12	VDC
	12V input	-0.7	--	25	
	24V input	-0.7	--	50	
	48V input	-0.7	--	100	
Start-up Voltage	5V input	3.5	4	4.5	
	12V input	4.5	8	9	
	24V input	11	16	18	
	48V input	24	33	36	
Input Filter				C Filter	

OUTPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Output Power		0.15	--	3	W
Output Voltage Accuracy	5% to 100% load	--	±1	±3	%
	No load	--	±1.5	±5	
Output Voltage Balance	Dual output, balanced loads	--	±0.5	±1	
Line Regulation	Full load, Input voltage from low to high	--	±0.2	±0.5	
Load Regulation	5% to 100% load	--	±0.6	±1	
Transient Recovery Time	25% load step change	--	0.5	3	ms
Transient Response Deviation		--	±2.5	±5	%
Transient Response Deviation	100% load	--	±0.02	±0.03	%/°C
Ripple*	20MHz Bandwidth	--	30	45	mVp-p
Noise*		--	35	75	
Output Power Protection		120	--	--	%
Output Short Circuit Protection				Continuous, automatic recovery	

Note: Dual output models unbalanced load: ±5%.

* Ripple and noise tested by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

COMMON SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Tested for 1 minute and leakage current less than 1 mA	1500	--	--	VDC
Isolation Resistance	Test at 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input/Output, 100KHz/1V	--	120	--	pF
Switching Frequency	100% load, Stand Input voltage	--	250	--	KHz
MTBF	MIL-HDBK-217F@25	1000	--	--	K hours
Case Material				Plastic (UL94-V0)	
Weight		--	4.92	--	g

ENVIRONMENTAL SPECIFICATIONS

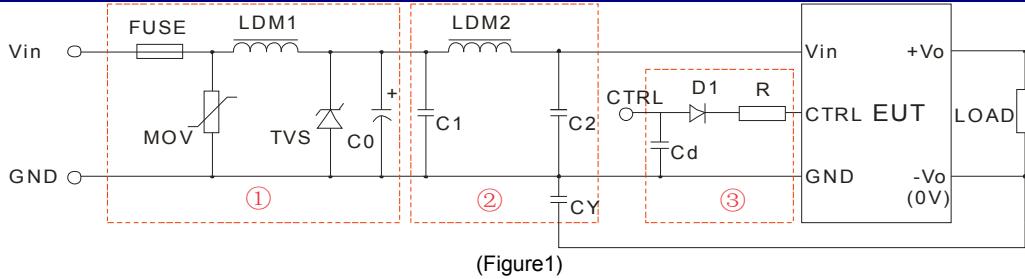
Item	Test Conditions	Min.	Typ.	Max.	Unit
Storage Humidity	Non condensing	--	--	95	%
Operating Temperature	Power derating (above 85°C)	-40	--	85	°C
Storage Temperature		-55	--	125	
Temp. rise at full load	Ta=25°C	--	25	--	
Lead Temperature	1.5mm from case for 10 seconds	--	--	300	
Cooling				Free air convection	

EMC SPECIFICATIONS

EMI	CE	CISPR22/EN55022	CLASS B (External Circuit Refer to Figure1-① or Figure 3)
	RE	CISPR22/EN55022	CLASS B (External Circuit Refer to Figure1-② or Figure 3)
EMS	ESD*	IEC/EN61000-4-2	Contact $\pm 4\text{KV}$ Air $\pm 8\text{KV}$ perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m perf. Criteria A
	EFT	IEC/EN61000-4-4	$\pm 2\text{KV}$ perf. Criteria B (External Circuit Refer to Figure1-③)
		IEC/EN61000-4-4	$\pm 4\text{KV}$ perf. Criteria B (External Circuit Refer to Figure 3)
	Surge	IEC/EN61000-4-5	$\pm 2\text{KV}$ perf. Criteria B (External Circuit Refer to Figure1-④ or Figure 3)
	CS	IEC/EN61000-4-6	3 Vr.m.s perf. Criteria A
Voltage dips, short and interruptions immunity		IEC/EN61000-4-29	0%-70% perf. Criteria B

Note: *SWRA/B24xxS-3WR2(Without External Circuit) CTRL pin only can meet ESD Contact $\pm 2\text{KV}$.

EMC RECOMMENDED CIRCUIT



(Figure1)

Recommended external circuit parameters:

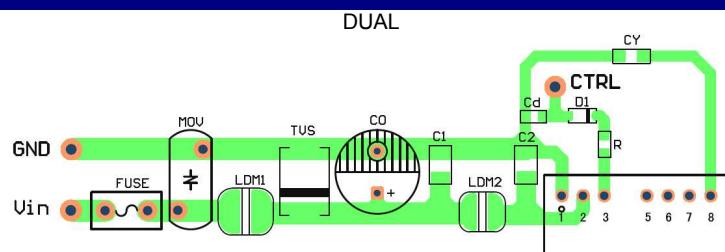
Model	Vin: 5V(designing)	Vin:12V	Vin:24V	Vin:48V
FUSE	Choose according to practical input current			
MOV	--	10D560	10D101	
LDM1	--	56 μH	56 μH	
TVS	SMCJ28A	SMCJ48A	SMCJ90A	
C0	680 $\mu\text{F}/25\text{V}$	120 $\mu\text{F}/50\text{V}$	120 $\mu\text{F}/100\text{V}$	
C1	4.7 $\mu\text{F}/50\text{V}$	4.7 $\mu\text{F}/50\text{V}$	4.7 $\mu\text{F}/100\text{V}$	
LDM2	12 μH	12 μH	12 μH	
C2	4.7 $\mu\text{F}/50\text{V}$	4.7 $\mu\text{F}/50\text{V}$	4.7 $\mu\text{F}/100\text{V}$	
CY	1nF/2KV	1nF/2KV	1nF/2KV	
D1	RB160M-60/1A			
R	Follows: $R = \frac{V_C - V_D - 1.0}{I_C} - 300$			
Cd	47nF/100V			

Note: 1. In Figure 1, part ① is EMS recommended external circuit, part ② is EMI recommended external circuit. Choose according to requirements.

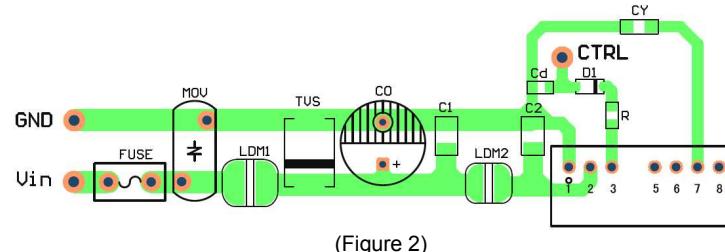
2. If want to meet ESD Contact $\pm 4\text{KV}$, CTRL pin must connect part ③.

3. If there is no recommended parameters, the model no require the external component.

EMC RECOMMENDED CIRCUIT PCB LAYOUT



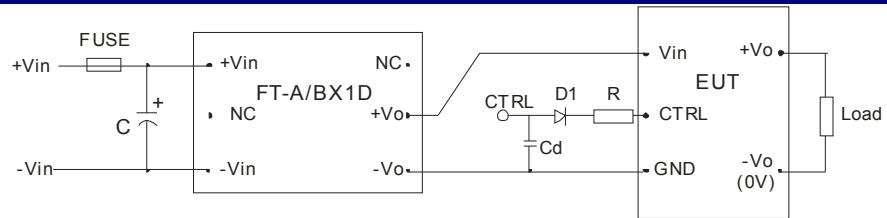
DUAL



SINGLE

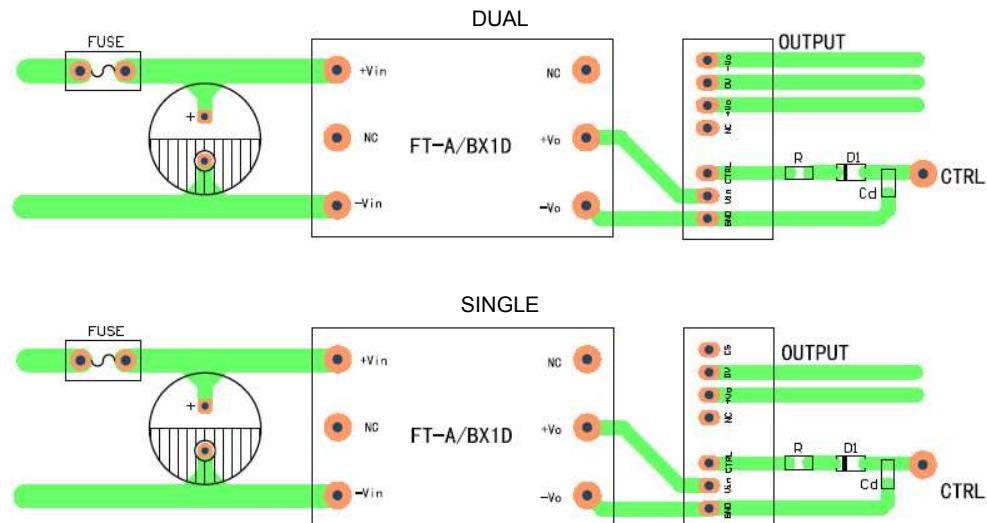
(Figure 2)

EMC MODULE RECOMMENDED CIRCUIT



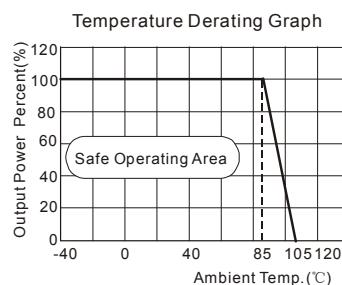
Nominal Voltage<48V, C \geq 330uF/50V
 Nominal Voltage =48V, C \geq 330uF/100V
 (Figure 3)

EMC MODULE RECOMMENDED CIRCUIT PCB LAYOUT

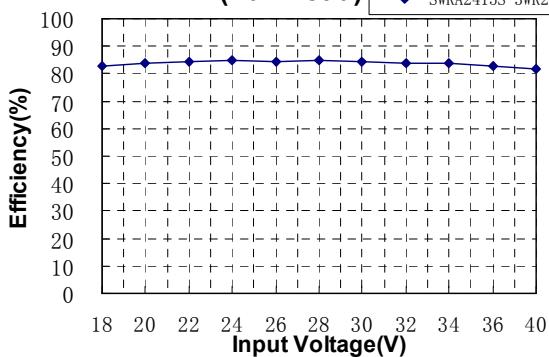


(Figure 4)

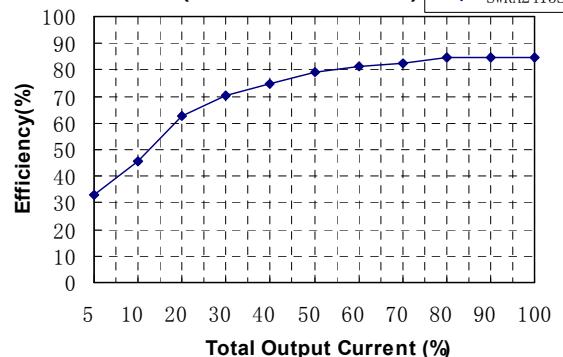
PRODUCT TYPICAL CURVE

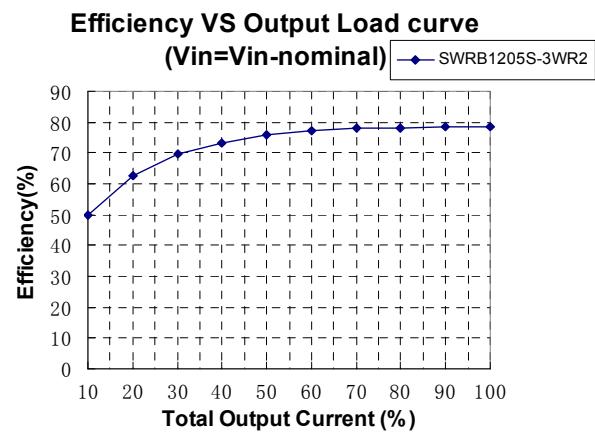
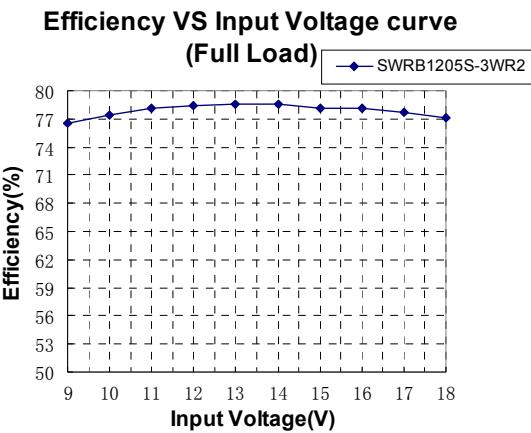


Efficiency VS Input Voltage curve
 (Full Load) SWRA2415S-3WR2

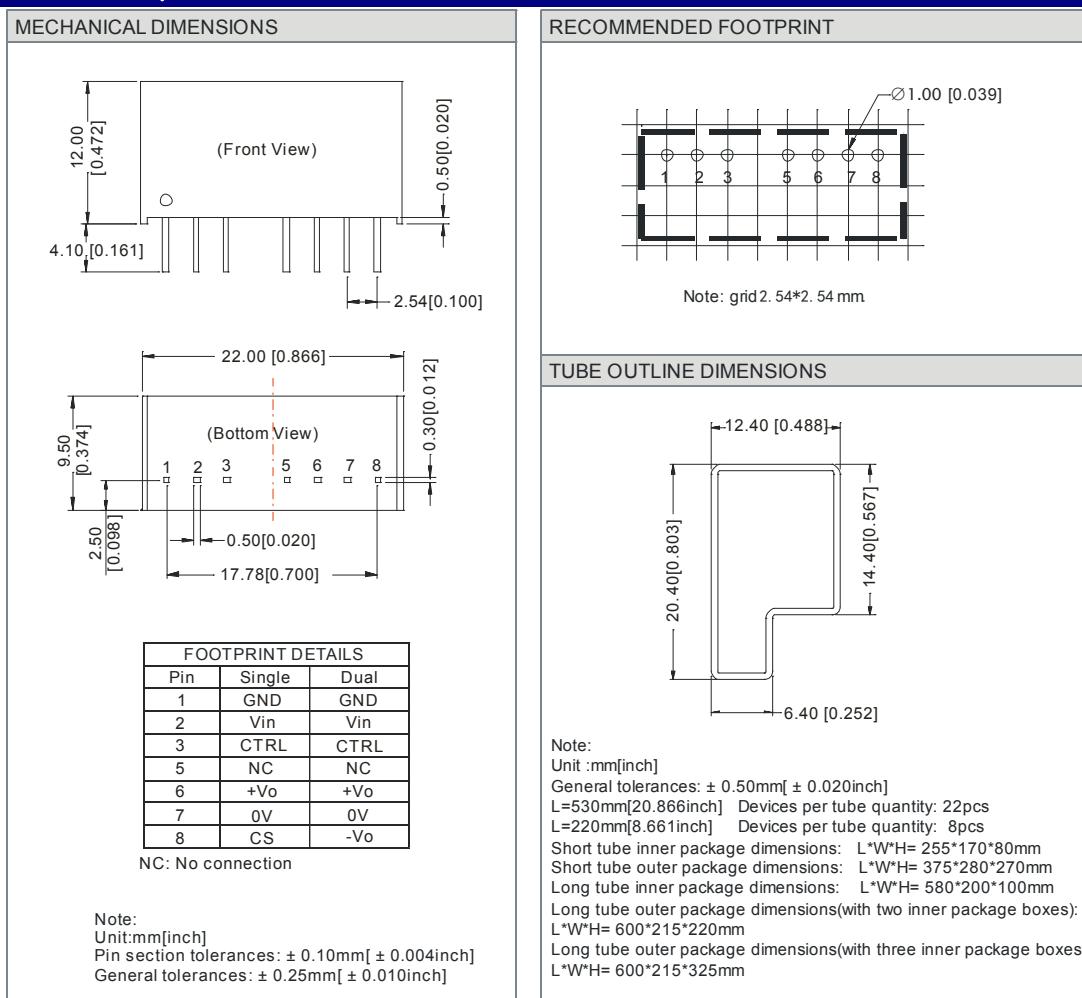


Efficiency VS Output Load curve
 (Vin=Vin-nominal) SWRA2415S-3WR2





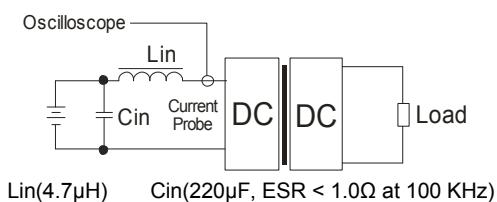
OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING



TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.



DESIGN CONSIDERATIONS

1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load could not be less than 5% of the full load. otherwise ripple maybe increase dramatically. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, suppose to use the resistance of 5% rated power,or use our company's products with a lower rated output power.

2) Recommended circuit

All the SWRA_S-3WR2 & SWRB_S-3WR2 Series have been tested according to the following recommended testing circuit before leaving factory (see Figure 5).

If you want to further decrease the input/output ripple, you can increase a capacitance properly or choose capacitors with low ESR. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor must less than the Max. Capacitive Load.

General: Cin: 5V&12V 100μF

24V&48V 10μF

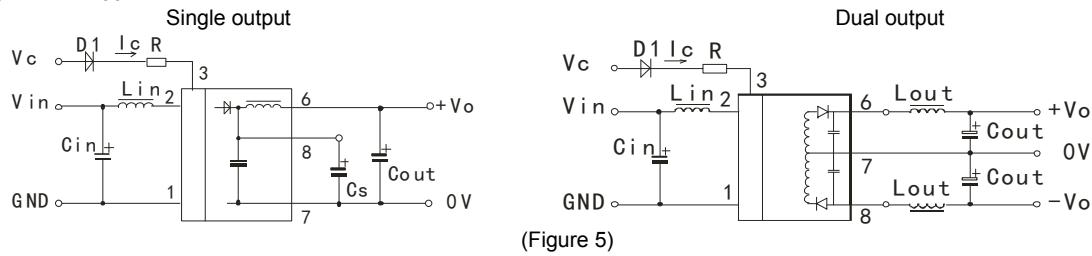
Lin: 4.7μH~120μH

Cs: 10μF~22μF

Cout: 100μF(Typ.)

Lout: 2.2μH~10μH

Cd: 47nF/100V



(Figure 5)

3) CTRL Terminal

When open or high impedance, the converter work well; When this pin is 'high'; the converter shutdown; It should be note that the input current should between 5-10mA, exceeding the maximum 20mA will cause permanence damage to the converter. The value of R can be derived as follows:

$$R = \frac{V_c - V_D - 1.0}{I_c} - 300$$

4) Input current

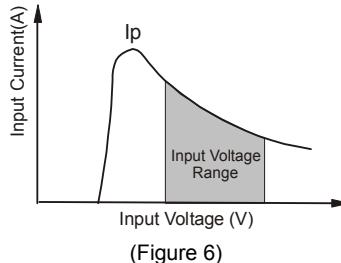
When it is used in unregulated power supply, be sure that the fluctuating range of the power supply and the rippled voltage do not exceed the module standard. Input current of power supply should afford the flash startup current of this kind of DC/DC module (Figure 6).

General: Vin:5V Ip =1200mA

Vin:12V Ip =650mA

Vin:24V Ip =310mA

Vin:48V Ip =160mA



(Figure 6)

5) Cannot use in parallel and hot swap

Note:

1. Min. load shouldn't be less than 5%, otherwise ripple maybe increase dramatically. Operation under minimum load will not damage the converter, however, they may not meet all specification listed.
2. Max. Capacitive Load tested at input voltage range and full load.
3. All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
4. In this datasheet, all the test methods of indications are based on our corporate standards.
5. All characteristics are for listed model, non-standard models may perform differently, please contact our technical person for more detail.
6. Contact us for your specific requirement.
7. Specifications subject to change without prior notice.