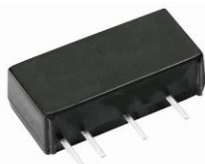




SB0505S-3WR2

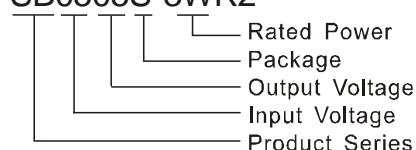
**3W, FIXED INPUT, ISOLATED & UNREGULATED
SINGLE OUTPUT DC-DC CONVERTER**



Continuous Short
Circuit Protection

PART NUMBER SYSTEM

SB0505S-3WR2



FEATURES

- Miniature SIP package
- Efficiency up to 85%
- High power density
- 1500VDC isolation
- Operating temperature range:
-40°C~+85°C
- No external component required
- Industry standard pinout

APPLICATIONS

The SB0505S-3WR2 is designed for application where isolated output is required from a distributed power system.

These products apply to where:

- 1) Input voltage range: $\pm 10\%V_{in}$;
- 2) 1500VDC input and output isolation;
- 3) Regulated and low ripple noise is not required.

Such as: digital circuits, low frequency analog circuits, and relay drive circuit.

SELECTION GUIDE

Model	Input Voltage(VDC)	Output Voltage (VDC)	Output Current (mA)		Input Current (mA)(Typ.)		Reflected Ripple Current (mA,Typ.)	Max. Capacitive Load ^① (μF)	Efficiency (% ,Typ.) @Max. Load
	Nominal (Range)		Max.	Max.	@Max. Load	@No Load			
SB0505S-3WR2	5 (4.5-5.5)	5	600	60	706	25	15	220	85

INPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Input Surge Voltage (1sec.max.)	5VDC input	-0.7	--	9	VDC
Input Filter		Capacitance Filter			

OUTPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Output Voltage Accuracy		See tolerance envelope curve			
Line Regulation	For V_{in} change of $\pm 1\%$	--	--	± 1.2	%
Load Regulation	10% to 100% load	--	8	--	
Temperature Drift	Full load	--	--	± 0.03	%/°C
Ripple & Noise*	20MHz bandwidth	--	100	--	mVp-p
Short Circuit Protection		Continuous, automatic recovery			

Note: *Ripple and noise tested by "parallel cable" method. See detailed operation instructions at DC-DC Application Notes.

COMMON SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Input-Output, tested for 1 minute and leakage current less than 1 mA	1500	--	--	VDC
Isolation Resistance	Input-Output, test at 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input-Output, 100KHz/0.1V	--	20	--	pF
Switching Frequency	100% load, Input voltage range	--	100	300	KHz
MTBF	MIL-HDBK-217F @25°C	3500	--	--	K hours
Case Material		Plastic (UL94-V0)			
Weight		--	2.4	--	g

ENVIRONMENTAL SPECIFICATIONS

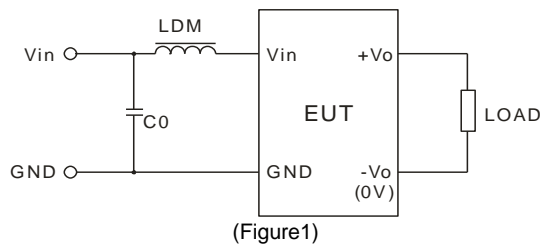
Item	Test Conditions	Min.	Typ.	Max.	Unit
Storage Humidity	Non condensing	--	--	95	%
Operating Temperature	Power derating (above 85°C, see Figure 2)	-40	--	105	°C
Storage Temperature		-55	--	125	
Temp. rise at full load	Ta=25°C	--	25	--	
Lead Temperature	1.5mm from case for 10 seconds	--	--	300	
Cooling		Free air convection			

EMC SPECIFICATIONS

EMI	CE	CISPR22/EN55022 CLASS B (Recommended Circuit Refer to Figure1)
	RE	CISPR22/EN55022 CLASS B (Recommended Circuit Refer to Figure1)
EMS	ESD	IEC/EN61000-4-2 Contact ±6KV perf. Criteria B

EMC RECOMMENDED CIRCUIT

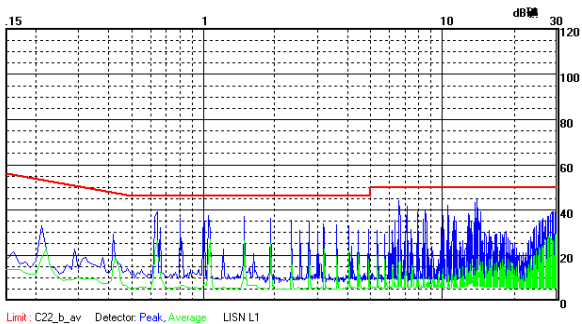
EMI Typical Recommended Circuit (CLASS B):



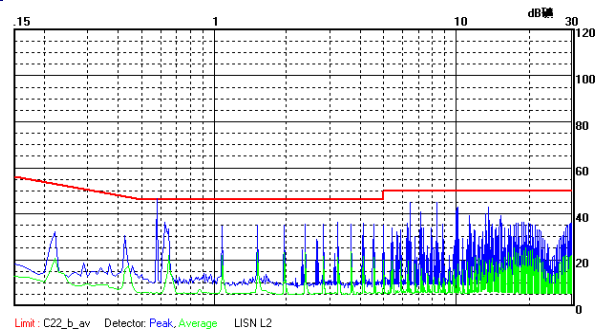
Recommended external circuit parameters:

Vin(V)		5
EMI	C0	4.7μF /50V
	LDM	6.8μH

EMI TEST WAVEFORM (RECOMMENDED CIRCUIT FIGURE 1)

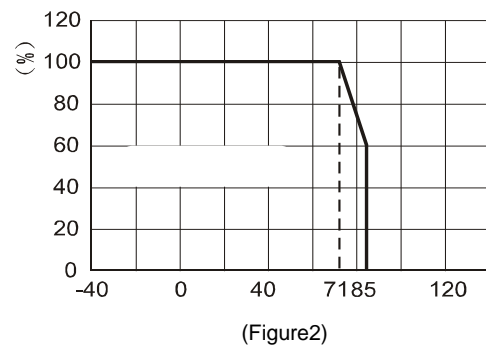
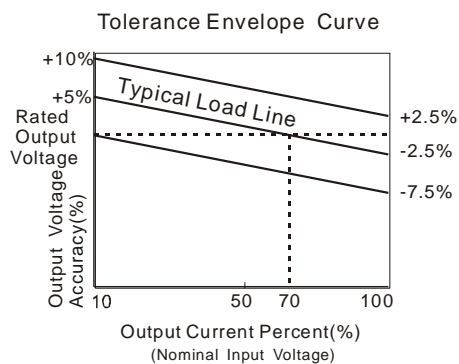


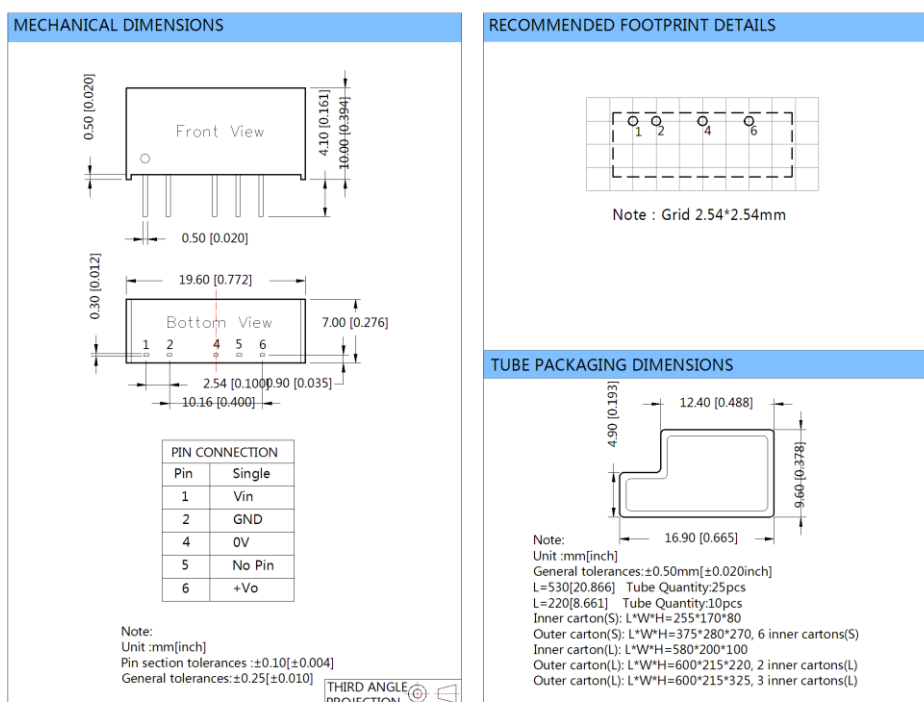
SB0505S-3WR2 CE(Class B, Positive line)



SB0505S-3WR2 CE(Class B, Negative line)

PRODUCT TYPICAL CURVE

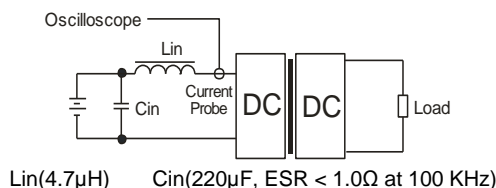




TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor L_{in} and Capacitor C_{in} to simulate source impedance.



DESIGN CONSIDERATIONS

1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load is not less than 10% of the full load. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power.

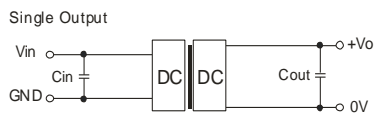
2) Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to add a circuit breaker to the circuit.

3) Recommended Circuit

If you want to further decrease the input/output ripple, a capacitor filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 3).

It should also be noted that the capacitance of filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the recommended capacitance of its filter capacitor sees (Table 1).



(Figure 3)

EXTERNAL CAPACITOR TABLE (Table 1)

Vin (VDC)	Cin (µF)	Single Vout (VDC)	Cout (µF)
5	4.7	3.3/5	10

Note: # for each output. It's not recommended to connect any external capacitor in the application field with less than 0.5 watt output.

4) The input and the output of the product are recommended to be connected to ceramic capacitor or electrolytic capacitor. Using tantalum capacitor may cause risk of failure

5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

1. Operation under minimum load will not damage the converter; However, they may not meet all specifications.
2. Max. Capacitive Load is tested at nominal input voltage and full load.
3. Unless otherwise noted, All specifications are measured at $T_a=25^{\circ}\text{C}$, humidity<75%, nominal input voltage and rated output load.
4. In this datasheet, all test methods are based on our corporate standards.
5. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more detail.
6. Please contact our technical support for any specific requirement.
7. Specifications of this product are subject to changes without prior notice: